

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph [0007] as follows:

[0007] The four power field effect transistors (FETs) PF1 to PF4 (hereinafter, ~~power FET PF1, PF2, PF3 and PF4 are abbreviated as~~ PF1, PF2, PF3 and PF4) are divided into two pairs of serially connected power FETs series-connectors, that is or, in other words, a series connector connection resulting from PF1 and PF3 and a series connector connection resulting from PF2 and PF4. They are respectively connected between the power supply +B and the ground having and have polarities as shown in the drawing. Diode D31 is connected in the forward direction with respect to the power supply +B between the power supply +B and the two ~~series-connectors pairs of serially connected power FETs~~ (PF1, PF3, PF2, PF4). The coil (motor coil) L1 that is subject to driving is connected between the junction of PF1 and PF3 and the junction of PF2 and PF4.

Please amend paragraph [0023] as follows:

[0023] The booster circuit for a pre-drive circuit may be equipped with a first and second boost control switching elements, a first and second capacitor, and first and second diodes. The first boost control switching element turns ON when a first end side of a motor coil reaches a higher electric potential than another end side and charges a first capacitor from a direct current power supply via a first diode. The second boost control switching element turns ON when the another end side of the motor coil reaches a higher electric potential than the first end side and ~~mutually~~ conducts electricity between the terminal on the side of the connection between the first boost control switching element and the first capacitor and the direct current power supply. This increases the electric potential ~~of~~ at the side of the junction of the first capacitor and the first diode.

Please amend paragraph [0025] as follows:

[0025] Boost voltage output is obtained from the connection path ~~of~~ between the second diode and the second capacitor.

Please amend paragraph [0026] as follows:

[0026] Parallel circuits of a diode and a resistor are respectively inserted and connected between the first end side of the motor coil and a control terminal of the first boost control switching element and between the another end side of the motor coil and a control terminal of the second boost control switching element, wherein the A-diode that faces the forward direction with respect to the control terminals of the respective boost control switching elements and a resistor parallel circuit are respectively inserted and connected between the first end side of the motor coil and the control terminal of the first boost control switching element and between the another end side of the motor coil and the control terminal of the second boost control switching element.

Please amend paragraph [0045] as follows:

[0045] In addition, ~~the positive electrode of~~ a capacitor C12 is connected between the junction of diodes D13 and D12 and the emitter of transistor Q11 with the positive electrode of capacitor C12 connected to the junction of diodes D13 and D12.

Please amend paragraph [0062] as follows:

[0062] In this drawing, ~~explaining~~ portions that are identical to or correspond to those in FIG. 1 will be explained using the same ~~codes~~ reference numbers. ~~in~~ In this embodiment, the RC filter circuit RCF has been omitted from the circuit shown in FIG. 1.

Please amend paragraph [0066] as follows:

[0066] As should now be appreciated, the above discussed booster circuit of a pre-drive circuit is formed using two ~~main~~ boost control switching elements having a respective Zener diode connected thereto to absorb (restrict voltage) the surge and protect the transistors that serve as the switching elements. ~~Also, a diode-capacitor pair is provided for each switching element for reverse current protection.~~ Further, a parallel circuit having a diode and ~~two resistors~~ a resistor is connected between each switching element and a junction of the motor coil and a series connection of a power FET pair. The boost voltage may be conducted through an optional RCF filter.